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SERIAL NUMBER 10/690,238	FILING DATE 10/21/2003 RULE	CLASS 716	GROUP ART UNIT 2825	ATTORNEY DOCKET NO. YOR920030315US1
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** CONTINUING DATA *****

** FOREIGN APPLICATIONS *****

IF REQUIRED, FOREIGN FILING LICENSE GRANTED

** 01/20/2004

Foreign Priority claimed	<input type="checkbox"/> yes <input checked="" type="checkbox"/> no	STATE OR COUNTRY	SHEETS	TOTAL	INDEPENDENT
35 USC 119 (a-d) conditions met	<input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> Met after Allowance	MD	13	28	3
Verified and Acknowledged	<i>None</i> <i>CH</i> <i>CH</i> Examiner's Signature Initials				

ADDRESS

33233
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TITLE

Computer aided design method and apparatus for modeling and analyzing on-chip interconnect structures

<input type="checkbox"/> All Fees
<input type="checkbox"/> 1.16 Fees (Filing)